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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/609,989	06/30/2003	Xiao M. Gao	ITL.0933US (P15730)	. 1067
21906 TROP PRUNE	7590 06/13/2007 R & HU, PC		EXAM	EXAMINER  AMAL, ALEXANDER  PAPER NUMBER  DELIVERY MODE
1616 S. VOSS ROAD, SUITE 750		•	JAMAL, ALEXANDER	
HOUSTON, T	X //05/-2631		ART UNIT PAPER NUMBER	
			2614	
			MAIL DATE	DELIVERY MODE
	•	•	06/13/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)			
	10/609,989	GAO ET AL.			
Office Action Summary	Examiner	Art Unit			
	Alexander Jamal	2614			
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the c	orrespondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DOWN THE MAILING DOWN THE MAILING DOWN THE MAILING DOWN THE MAILING THE M	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tin will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).			
Status					
1) Responsive to communication(s) filed on 10 A	pril 2007.				
	action is non-final.				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11,.45	53 O.G. 213.			
Disposition of Claims					
4) ☐ Claim(s) is/are pending in the application 4a) Of the above claim(s) is/are withdraw 5) ☐ Claim(s) is/are allowed. 6) ☑ Claim(s) 1-5,7-18 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	vn from consideration.				
Application Papers					
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) access applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Examine	epted or b) objected to by the Edrawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
<ul> <li>12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:</li> <li>1. Certified copies of the priority documents</li> <li>2. Certified copies of the priority documents</li> <li>3. Copies of the certified copies of the priori application from the International Bureau</li> <li>* See the attached detailed Office action for a list of the certified copies of the certified copies of the priori application from the International Bureau</li> </ul>	s have been received. s have been received in Application ity documents have been received (PCT Rule 17.2(a)).	on No In this National Stage			
Attachment(s)					
Notice of References Cited (PTO-892)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  Information Disclosure Statement(s) (PTO/SB/08)  Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	te			

## **DETAILED ACTION**

## Response to Amendment

- 1. Based upon the submitted amendment, the examiner notes that claims 1,10,14 are amended and claim 6 is cancelled.
- 2. Examiner submits a new set of non-final rejections based on newly discovered prior art.
- 3. Examiner further notes patent to Harnett (USRE39051E) that discloses using a fuzzy inference system to match impedances.

## Claim Rejections - 35 USC § 103

1. Claims 1-5,7-18 rejected under 35 U.S.C. 103(a) as being unpatentable over Shi et al. (US2004/0101130A1), and further in view of Jeffery et al. (6970905).

As per claim 1, Shi discloses a system comprising a signal generator (inherently comprised in Xmit path 62 in Fig. 9D to provide the transmitted stimulus disclosed in page 11 paragraph 121), impedance mismatch hardware (switches 82a,82b,86a,86b) coupled to impedances R4,R3 in Fig. 9D), and a controller (DSP disclosed in page 12 paragraph 130) is used to measure subscriber loop characteristics to determine DSL capability (page 1 paragraph 3). However, Shi does not disclose that the DSP implements a fuzzy inference system.

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Jeffery discloses a DSL system that monitors and analyzes measured conditions on the DSL line, including cable impedance and signal to noise ratio. Jeffery teaches that specialized logic, such as Fuzzy Logic may be used by the system in order to select the optimum configuration for the DSL modem (Col 15 lines 1-15). It would have been obvious to one of ordinary skill in the art at the time of this application to implement fuzzy logic in the controller of Shi for the purpose of providing the optimum configuration and results for the DSL tests. The DSP of Shi, when operating with fuzzy logic will be a fuzzy inference system.

As per **claim 10**, claim rejected for the same reasons as claim 1. FDR and TDR methods use the echo delay is used to determine the loop characteristics (page 11 paragraphs 119,121).

As per claim 14, it is rejected for the same reasons as claim 10. The DSP (page 11 paragraph 119) inherently comprises software for the purpose of controlling the hardware. The DSP controller of Shi in view of Jeffery's teachings, is a fuzzy inference system that adjusts the impedance seen by reflected signals by activating or deactivating (via switches) the hybrid or termination circuitry. This will function to modify the received signals because the impedance will be different.

As per claim 2, the impedance comprises a resistance (R3,R4).

As per **claim 3**, the system comprises an active termination impedance (page 11 paragraph 120).

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As per **claim 4**, the receive signal the echo measured in either the FDR, or TDR method) is modified when the active termination impedance and hybrid are activated or deactivated (page 11 paragraphs120-124).

As per **claims 5,9**, the FDR and TDR tests measure the loop length and impedance which determine the ability to run a DSL on the loop (page 11 paragraph 119).

As per claims 7, the DSP controller is a fuzzy inference system that adjusts the impedance seen by reflected signals by activating or deactivating (via switches) the hybrid or termination circuitry. This will function to modify the received signals because the impedance will be different.

As per **claim 8,** FDR and TDR methods use the echo delay is used to determine the loop characteristics (page 11 paragraphs 119,121). The amplitudes and time delay of the reflected signals is measured (page 11 paragraph 121).

As per claims 11,15, claim rejected for the same reasons as claims 6-8.

As per claim 12, claim rejected for the same reasons as claim 5. Additionally loop taps may be determined via measurements of standing waves. The resonant frequencies will indicate loop taps (impedance mismatches), and the loop length, which itself is a determination of the loop impedance, which is an indication of the insertion loss.

As per **claim 13**, the loop characterisitics are used to measure subscriber loop characteristics to determine DSL capability (page 1 paragraph 3).

As per claims 16-18, claim rejected for the same reasons as claims 8,12,13.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alexander Jamal whose telephone number is 571-272-7498. The examiner can normally be reached on M-F 9AM-6PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Curtis A Kuntz can be reached on 571-272-7499. The fax phone numbers for the organization where this application or proceeding is assigned are 571-273-8300 for regular communications and 571-273-8300 for After Final communications.

Examiner Alexander Jamal

June 7, 2007